



**Jawaharlal Nehru Technological University Anantapur**  
**Examination Branch**  
**Timetable**

**M.Tech - I Semester Supplementary Examinations March/April 2010**  
 (for 2004-05 admitted batches only)

Time: 10.00 AM to 1.00 PM

Date / Day	Computer Science	Electrical Power Engineering	Electrical Power Systems	Power Electronics	Digital Systems and Computer Electronics (DSCE)
29-03-2010 Monday	Computer Communications	Power System Dynamics	Electrical Distribution & Automation	Machine Modeling & Analysis	Digital System Design
31-03-2010 Wednesday	Data Structures & Algorithm Analysis	Solid State Power Converters	Reliability Engineering	Solid State Power Converters	VLSI Technology & Design
03-04-2010 Saturday	Computer System Architectures	Digital Control of Systems	Advanced Microprocessors	Digital Control Systems	Digital Data Communications
07-04-2010 Wednesday	Centralized & Distributed Data Base Systems	Advanced Microprocessors	FACTS Controllers & their Applications	Advanced Microprocessors	Advanced Operating Systems
09-04-2010 Friday	Modern Operating Systems	Power System Operation & Control	Advanced Control Systems	Special Machine & Control	Speech Processing
				Solid State Relay & Static VAR Compensation	
12-04-2010 Monday	Discrete Structures & Automata Theory	----	Energy Auditing Conservation & Management	Energy Conversion Systems	C++ and JAVA Programming
15-04-2010 Thursday	----	HVDC Transmission	-----	HVDC Transmission	-----

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Date / Day	Embedded Systems	VLSI System Design	Software Engineering	CAD/CAM
29-03-2010 Monday	Micro Controllers for Embedded System Design	Computational Technologies in Micro Electronics	Distributed Systems	Numerical Methods for Partial Differential Equations
31-03-2010 Wednesday	Analog & Digital IC Design	VLSI Technology	Network Security	Design for Manufacturing
03-04-2010 Saturday	Simulation & Modeling	Digital System Design with Programming Logic	Object Oriented Analysis & Design through UML	Stress Analysis & Vibration
07-04-2010 Wednesday	Embedded System Concepts	VHDL Analysis & Design of Digital Systems	Software Process Management	Design Optimization
09-04-2010 Friday	Principles of VLSI Design	---	Unix Internals	Finite Element Analysis
12-04-2010 Monday	Image & Video Processing	Analog VLSI	Software Testing	Computer Aided Design
15-04-2010 Thursday	---	Electronic Design Automation Tools	---	---

NOTE: 1. Any clashes / omissions in this time-table may be brought to the notice of the under signed immediately.  
2. If the Government declares holiday on any of the above dates, the examinations will be conducted as usual.

Notification Date: 22-02-2010  
Revised on 22-03-2010

Sd.  
Controller of Examinations i/c



**Jawaharlal Nehru Technological University Anantapur**  
**Examination Branch**  
**Timetable**

**M.Tech - II Semester Supplementary Examinations March/April 2010**  
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Time: 10.00 AM to 1.00 PM

Date / Day	Computer Science	Electrical Power Engineering	Electrical Power Systems	Power Electronics	Digital Systems and Computer Electronics
30-03-2010 Tuesday	Network Security	Voltage Stability	Power System Control & Stability	Power Electronics Control of Drives	Artificial Neural Networks
	Mobile Communications				
	Network Management				
	Artificial Neural Networks				
01-04-2010 Thursday	Object Oriented Analysis & Design using UML	Advanced Power System Protection	Advanced Power System Protection	Electric Traction Drives	Design of Fault Tolerant Systems
06-04-2010 Tuesday	Data Ware Housing & Mining	Flexible AC Transmission Systems	Power System Reliability	Flexible AC Transmission Systems	VHDL Programming
08-04-2010 Thursday	Software Engineering	Distribution Automation	High Voltage DC Transmission	Industrial Application of Power Electronics	Digital Image Processing
10-04-2010 Saturday	Language processors	Artificial Neural Networks Applications	Neural Networks & Fuzzy Systems	Digital Simulation of Power Electronic Circuits & Systems	System Simulation & Modelling
	Programming Languages			Artificial Neural Networks Applications	
	Unix Internals				
	Advanced Unix Programming				
13-04-2010 Tuesday	Software Testing	Power Quality	EHV AC Transmission	Reliability Engineering	Micro Computer System Design
	Pattern Recognition			Operations Research	
	Digital Image Processing				
	Pattern Recognition & Image Analysis				

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Date / Day	Embedded Systems	VLSI System Design	Software Engineering	CAD/CAM
30-03-2010 Tuesday	Real Time Operating Systems for Embedded Systems	Physical Design Automation	Architecture for Software Systems	Computer Aided Manufacturing
01-04-2010 Thursday	Advanced Digital Filtering	Mixed Signal Design	Data Warehousing & Mining	Computational Fluid Dynamics
06-04-2010 Tuesday	DSP Processors & Architectures	VLSI System Design & Testing	Software Metrics	Flexible Manufacturing Systems
08-04-2010 Thursday	Embedded Software	Advance Digital Design	Software Project Management	Design of Hydraulic & Pneumatic Systems
10-04-2010 Saturday	CPLD & FPGA Architecture & Applications	Low Power VLSI Circuits Modeling & Synthesis with Verilog HDL	Design Patterns	Automation in Manufacturing
13-04-2010 Tuesday	Wideband Communication	DSP Architecture & Applications	Pattern Recognition	Industrial Robotics

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