



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR
ANANTAPUR - 515002 A.P. (INDIA)

Examination Branch

Notification

for

M.Tech I & II Semesters Supplementary Examinations March/April 2010

(For 2004-05 admitted batches only)

LAST CHANCE
FOR
2004-05 admitted batches only

Centre of Examination: JNTUA College of Engineering Anantapur, Anantapur.

The students appearing for the above examinations commencing from **29.3.2010** are informed that the applications will be received as per the time schedule given below. The detailed time tables will be notified later.

Last Date without fine	15.03.2010
Last Date with Rs.100/= fine	17.03.2010
Last Date with Rs.1000/= fine	24.03.2010

Examination Fee		
1.	Each theory/practical subject	Rs. 500-00
2.	Memorandum of Marks	Rs.10-00
3.	Cost of application	Rs.5-00

- Note:
1. Applications are available at the office of the Principal.
 2. Duly filled-in applications along with the photocopies of marks sheets of earlier attempts, duly attested by the college principal should be handed over in the college office with the necessary fee.
 3. Hall tickets will be issued only to the eligible candidates who fulfill the academic requirements of the University.

Sd.
Director of Evaluation i/c

Date: 22-02-2010

Note:	1. For applications without the late fee the DD should be dated 17.03.2010 and the filled in applications have to be submitted at the University examination branch on 19.03.2010.
	2. For applications with the late fee of Rs.100.00 the DD should be dated 18.03.2010 and the filled in applications have to be submitted at the University examination branch on 19.03.2010.
	3. For applications with the late fee of Rs.1000.00 the DD should be dated 25.03.2010 and the filled in applications have to be submitted at the University examination branch on 26.03.2010.



Jawaharlal Nehru Technological University Anantapur
Examination Branch
Timetable

M.Tech - I Semester Supplementary Examinations March/April 2010
(for 2004-05 admitted batches only)

Time: 10.00 AM to 1.00 PM

Date / Day	Electrical Power Engineering	Electrical Power Systems	Power Electronics	Digital Systems and Computer Electronics (DSCE)
29-03-2010 Monday	Power System Dynamics	Electrical Distribution & Automation	Machine Modelling & Analysis	Digital System Design
31-03-2010 Wednesday	Solid State Power Converters	Reliability Engineering	Solid State Power Converters	VLSI Technology & Design
03-04-2010 Saturday	Digital Control of Systems	Advanced Microprocessors	Digital Control Systems	Digital Data Communications
07-04-2010 Wednesday	Advanced Microprocessors	FACTS Controllers & their Applications	Advanced Microprocessors	Advanced Operating Systems
09-04-2010 Friday	Power System Operation & Control	Advanced Control Systems	Special Machine & Control	Speech Processing
			Solid State Relay & Static VAR Compensation	
12-04-2010 Monday	----	Energy Auditing Conservation & Management	Energy Conversion Systems	C++ and JAVA Programming
15-04-2010 Thursday	HVDC Transmission	-----	HVDC Transmission	-----

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Jawaharlal Nehru Technological University Anantapur
Examination Branch
Timetable

M.Tech - I Semester Supplementary Examinations March/April 2010
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Time: 10.00 AM to 1.00 PM

Date / Day	Embedded Systems	VLSI System Design	Software Engineering	CAD/CAM
29-03-2010 Monday	Micro Controllers for Embedded System Design	Computational Technologies in Micro Electronics	Distributed Systems	Numerical Methods for Partial Differential Equations
31-03-2010 Wednesday	Analog & Digital IC Design	VLSI Technology	Network Security	Design for Manufacturing
03-04-2010 Saturday	Simulation & Modeling	Digital System Design with Programming Logic	Object Oriented Analysis & Design through UML	Stress Analysis & Vibration
07-04-2010 Wednesday	Embedded System Concepts	VHDL Analysis & Design of Digital Systems	Software Process Management	Design Optimization
09-04-2010 Friday	Principles of VLSI Design	---	Unix Internals	Finite Element Analysis
12-04-2010 Monday	Image & Video Processing	Analog VLSI	Software Testing	Computer Aided Design
15-04-2010 Thursday	---	Electronic Design Automation Tools	---	---

- NOTE: 1. Any clashes / omissions in this time-table may be brought to the notice of the under signed immediately.
2. If the Government declares holiday on any of the above dates, the examinations will be conducted as usual.

Dt: 22-02-2010

Sd.
Controller of Examinations i/c



Jawaharlal Nehru Technological University Anantapur
Examination Branch
Timetable

M.Tech - II Semester Supplementary Examinations March/April 2010
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Time: 10.00 AM to 1.00 PM

Date / Day	Electrical Power Engineering	Electrical Power Systems	Power Electronics	Digital Systems and Computer Electronics
30-03-2010 Tuesday	Voltage Stability	Power System Control & Stability	Power Electronics Control of Drives	Artificial Neural Networks
01-04-2010 Thursday	Advanced Power System Protection	Advanced Power System Protection	Electric Traction Drives	Design of Fault Tolerant Systems
06-04-2010 Tuesday	Flexible AC Transmission Systems	Power System Reliability	Flexible AC Transmission Systems	VHDL Programming
08-04-2010 Thursday	Distribution Automation	High Voltage DC Transmission	Industrial Application of Power Electronics	Digital Image Processing
10-04-2010 Saturday	Artificial Neural Networks Applications	Neural Networks & Fuzzy Systems	Digital Simulation of Power Electronic Circuits & Systems	System Simulation & Modelling
			Artificial Neural Networks Applications	
13-04-2010 Tuesday	Power Quality	EHV AC Transmission	Reliability Engineering	Micro Computer System Design
			Operations Research	

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Time: 10.00 AM to 1.00 PM

Date / Day	Embedded Systems	VLSI System Design	Software Engineering	CAD/CAM
30-03-2010 Tuesday	Real Time Operating Systems for Embedded Systems	Physical Design Automation	Architecture for Software Systems	Computer Aided Manufacturing
01-04-2010 Thursday	Advanced Digital Filtering	Mixed Signal Design	Data Warehousing & Mining	Computational Fluid Dynamics
06-04-2010 Tuesday	DSP Processors & Architectures	VLSI System Design & Testing	Software Metrics	Flexible Manufacturing Systems
08-04-2010 Thursday	Embedded Software	Advance Digital Design	Software Project Management	Design of Hydraulic & Pneumatic Systems
10-04-2010 Saturday	CPLD & FPGA Architecture & Applications	Low Power VLSI Circuits Modelling & Synthesis with Verilog HDL	Design Patterns	Automation in Manufacturing
13-04-2010 Tuesday	Wideband Communication	DSP Architecture & Applications	Pattern Recognition	Industrial Robotics

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