



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR
ANANTAPUR - 515002(A.P)**

Hall Ticket
Duplicate

EPS /EPE

**M.Tech. – II Semester
(For students admitted in 2009 – 2010 only)**

H.T.No:

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Centre

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- Name of the Candidate:
- Father's Name:
- Month and Year of Examination:
- Regular/Supplementary:

Please affix
your
photograph
duly attested by
the Principal

Subject for which registration is required:
Details of subjects registered: (Tick () the appropriate box)

1. 9D49201 FACTS <input type="checkbox"/>	5. 9D49205 Energy Conversion Systems <input type="checkbox"/>
2. 9D49202 HVDC Transmission <input type="checkbox"/>	6.9D49206a Programmable Logic Controllers <input type="checkbox"/>
3. 9D49203 Operation & Control of Power System <input type="checkbox"/>	7. 9D49206b Energy Auditing, Conservation & Management <input type="checkbox"/>
4. 9D49204 Advanced Power System protection <input type="checkbox"/>	8. 9D49207 Simulation Lab <input type="checkbox"/>

Signature of the Candidate

Principal

Controller of Examinations

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Signature of the Candidate

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Certificate

This is to certify thatbearing H.T.No.....

- I. is /was a student of this college during the year The facts mentioned by him /her in the application have been verified and found to be correct. He/ She had registered for all subjects above in the current semester / class.
- II. has put in% of attendance (applicable only for regular candidates).
- III. has not passed in earlier examinations in any of the subject in which he/ she has now registered.

Principal's remarks if any:

Date:

Signature of the Principal with seal

Prescribed Fees

For the whole examination: Rs.750/- (for regular examinations only)

For each Theory / Practical subject: Rs.500/- (for supplementary examinations only)

Memorandum of Marks: Rs .10/-

Cost of application: Rs.5/-

Note: No Application is accepted unless the candidate pays the prescribed fees along with the fees for marks memorandum within the stipulated time.

Instructions:

1. Candidates are instructed to be very careful about the entries to be made. All entries should be in candidate's own handwriting. Candidate will be held responsible for any incorrect entry that he/she makes.
2. The University reserves the right to cancel the admission of the candidate at any stage when it is detected that his/ her admission to the examination or the college is against rules.
3. Any false or incorrect statement in the application will render the candidate liable to disciplinary action.
4. A demand draft drawn on any nationalized bank for the examination fee including fee for memorandum of marks, in favor of the Registrar, JNT University Anantapur, payable at Anantapur should be enclosed to the application.
5. The application should be submitted through the Principal / Head of respective college /unit.

Note: Read instructions carefully

Instructions to the candidates

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4. Candidates are also prohibited from bringing their own mathematical tables into the examination hall. The Chief Superintendent upon request when answering papers in Mathematics and Science subjects will supply these to the candidates.
5. Mathematical instruments will however be allowed to be brought in. They are also prohibited from taking to each other, copy or communication with a person outside the examination hall. If found violating these rules and / or committing any other malpractice and / or behaving in an undisciplined manner or causing nuisance or disturbance to the other candidates, he/she will at once be expelled from the examination hall. He/She will not be allowed to sit for the remaining papers and he/she will be liable to be rusticated.
6. Candidates are permitted to bring Electronic Calculator into the examination hall.
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PE / PE & ED

H.T.No:

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Centre

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Subjects for which registration is required:

Details of subjects registered: (Tick () the appropriate box)

1. 9D49201 Flexible AC Transmission Systems <input type="checkbox"/>	5. 9D54205 Modern Power Electronics <input type="checkbox"/>
2. 9D49202 HVDC Transmission <input type="checkbox"/>	6. 9D49206a Programmable Logic Controllers <input type="checkbox"/>
3. 9D54203 Power Electronic Control of AC Drives <input type="checkbox"/>	7. 9D49206b Energy Auditing, Conservation & Management <input type="checkbox"/>
4. 9D54204 Advanced Power Semiconductor Devices & Protection <input type="checkbox"/>	8. 9D54207 Electrical Systems Simulation Lab <input type="checkbox"/>

Signature of the Candidate

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Memorandum of Marks: Rs .10/-

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VLSI SD/ VLSID

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Subject for which registration is required:

1. 9D55201 Testing & Testability	<input type="checkbox"/>	6. 9D57206a Nano Electronics	<input type="checkbox"/>
2. 9D57202 Low Power VLSI Design	<input type="checkbox"/>	7. 9D57206b Cryptography & Network Security	<input type="checkbox"/>
3. 9D57203 Algorithms for VLSI Design Automation	<input type="checkbox"/>	8. 9D57206c Real Time Operating Systems	<input type="checkbox"/>
4. 9D55204 FPGA Architectures & Applications	<input type="checkbox"/>	9. 9D57207 Mixed Signal Lab	<input type="checkbox"/>
5. 9D57205 Scripting Language for VLSI Design Automation	<input type="checkbox"/>		

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