



Jawaharlal Nehru Technological University Anantapur

Examination Branch

Timetable

M.Tech - I Semester Regular Examinations February 2010

(for students admitted in 2009-2010 only)

Time: 10.00 AM to 1.00 PM

Date / Day	Electrical Power Engineering (EPE) & Electrical Power Systems (EPS)	Power Electronics and Electrical Drives (PE&ED) & Power Electronics (PE)	VLSI Systems (VLSIS), VLSI System Design (VLSISD) and VLSI
15-02-2010 Monday	Modern Control Theory 9D49101 (Common to EPE, EPS, PE&ED & PE)		VLSI Technology 9D57101 (Common to VLSIS, VLSISD, VLSI & ES)
17-02-2010 Wednesday	Microprocessors & Microcontrollers 9D49102 (Common to EPE, EPS, PE&ED & PE)		Analog IC Design 9D57102
19-02-2010 Friday	Power System Control & Stability 9D49103	Principles of Machine Modeling Analysis 9D54103	Embedded System Concepts 9D57106a
22-02-2010 Monday	EHVAC Transmission 9D49104	Analysis of Power Electronic Converters 9D54104	Hardware Description Languages 9D57104
24-02-2010 Wednesday	Reactive Power Compensation & Management 9D49105	Power Electronic Control of DC Drives 09D54105	Hardware Software Co-design 9D57105
26-02-2010 Friday	Advanced Digital Signal Processing 9D49106a (Common to EPE, EPS, PE&ED & PE)		System Modeling & Simulation 9D57106b
			ASIC Design 9D57106c
03-03-2010 Wednesday	Neural Networks and Fuzzy Systems 9D49106b (Common to EPE, EPS, PE&ED & PE)		Digital IC Design 9D57103 (Common to VLSIS, VLSISD, VLSI & ES)

Contd...page 2



Jawaharlal Nehru Technological University Anantapur

Examination Branch

Timetable

M.Tech - I Semester Regular Examinations February 2010

(for students admitted in 2009-2010 only)

Time: 10.00 AM to 1.00 PM

Date / Day	Digital Systems and Computer Electronics (DSCE)	Digital Electronics and Communications Systems (DECS)	Embedded Systems (ES)
15-02-2010 Monday	Digital System Design 9D06101 (Common to DSCE & DECS)		VLSI Technology 9D57101 (Common to VLSIS, VLSISD, VLSI & ES)
17-02-2010 Wednesday	Embedded System Concepts 9D06102 (Common to DSCE, DECS, & ES)		
19-02-2010 Friday	Advanced Computer Architecture 9D06103 (Common to DSCE, DECS, & ES)		
22-02-2010 Monday	Advanced Data Communications 9D06104	Digital Communication Techniques 9D38104	Micro Controllers & Interfacing 9D55104
24-02-2010 Wednesday	Neural Networks & Applications 9D06105	Adaptive Signal Processing 9D38105	Operating Systems 9D55105
26-02-2010 Friday	DSP Processors & Architectures 9D06106b (Common to DSCE & DECS)		Advanced DSP & Applications 9D55101
	Lower Power VLSI Design 9D06106c (Common to DSCE & DECS)		
03-03-2010 Wednesday	Network Security & Cryptography 9D06106a	Advanced Digital Signal Processing 9D38103	Digital IC Design 9D57103 (Common to VLSIS, VLSISD, VLSI & ES)
			Algorithms for VLSI Design Automation 9D55106c

Contd...page 3



Jawaharlal Nehru Technological University Anantapur
Examination Branch
Timetable

M.Tech - I Semester Regular Examinations February 2010
(for students admitted in 2009-2010 only)

Time: 10.00 AM to 1.00 PM

Date / Day	Computer Science and Engineering (CSE) & Computer Science (CS)	Software Engineering (SE)	Machine Design (MD)	CAD/CAM
15-02-2010 Monday	Advanced Data Structures & Algorithms 9D58101 (Common to CSE, CS & SE)		Computational Methods 9DBS101 (Common to MD and CAD/CAM)	
17-02-2010 Wednesday	Discrete Structures & Automata Theory 9D58102	Advanced Software Engineering 9D25102	Advanced Mechanisms 9D15102	Finite Element Methods 9D04102
19-02-2010 Friday	Computer System Design 9D58103	Software Requirements & Estimation 9D25103	Advanced Mechanics of Solids 9D15103	Computer Integrated Manufacturing 9D04103
22-02-2010 Monday	Java & Web Technologies 9D58104	Software Metrics 9D25104	Fracture, Fatigue & Creep Deformation 9D15104	Geometric Modeling 9D04104
24-02-2010 Wednesday	Software Engineering 9D58105	Middleware Technologies 9D25105	Materials Technology 9D15105	Advances in Manufacturing Technology 9D04105
26-02-2010 Friday	Advances in Databases 9D58106a	Object Oriented Modeling 9D25106a	Tribology 9D15106a	Quality Engineering & Manufacturing 9D04106a
	Distributed Databases 9D58106b	Software Project Management 9D25106b	Gear Engineering 9D15106b	Computer Aided Process Planning 9D04106b
	Computer Vision 9D58106c	Software Process Management 9D25106c	Non Destructive Evaluation 9D15106c	Design for Manufacturing 9D04106c

- NOTE: 1. Any clashes / omissions in this time-table may be brought to the notice of the under signed immediately.
2. If the Government declares holiday on any of the above dates, the examinations will be conducted as usual.

Date: 15 - 01 - 2010

Sd.
Controller of Examinations i/c