



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR  
ANANTAPUR - 515002 A.P. (INDIA)**

**Examination Branch**

**Notification for M.Tech II Semester Regular Examinations October 2011**  
(For 2010-11 admitted batches only)

The students appearing for the above examinations commencing from 17.10.2011 are informed that the applications will be received as per the time schedule given below:

Without Late Fee	27.09.2011
With Late Fee of Rs. 100/-	29.09.2011
With Late Fee of Rs.1000/-	01.10.2011

EXAMINATION FEE		
1.	WHOLE EXAMINATION	Rs.750-00
2.	MEMORANDUM OF MARKS	Rs.10-00
3.	COST OF APPLICATION	Rs.5-00

- Note: (i) Applications are available at the office of the Principal and duly filled-in applications should be handed over in the college office along with necessary fee.  
(ii) Hall tickets will be issued only to the eligible candidates who fulfill the academic requirements of the University.

Sd/-  
DIRECTOR OF EVALUATION i/c

Date: 06-09-2011

- Note: For applications without late fine, the DDs are to be dated 28.09.2011 and filled in applications have to be submitted at the University Examination Branch on 10.10.2011 only.  
For applications with late fine of Rs.100.00, the DDs are to be dated 01.10.2011 and filled in applications have to be submitted at the University Examination Branch on 10.10.2011 only.  
For late fee of Rs. 1000.00, the DDs are to be dated 03.10.2011 and filled in applications have to be submitted at the University Examination Branch on 10.10.2011 only.



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR  
ANANTAPUR - 515002 A.P. (INDIA)**

**Examination Branch**

**Notification**  
**for**  
**M.Tech Supplementary October/November 2011 Examinations**  
**First Semester**  
**(For 2010, 2009, 2008 & 2007 admitted batches only)**  
**Second Semester**  
**(For 2009, 2008 & 2007 admitted batches only)**

The students appearing for the above examinations commencing from 17.10.2011 are informed that the applications will be received as per the time schedule given below:

Without Late Fee	27.09.2011
With Late Fee of Rs. 100/-	29.09.2011
With Late Fee of Rs.1000/-	01.10.2011

EXAMINATION FEE		
1.	EACH THEORY/PRACTICAL SUBJECT	Rs.500-00
2.	MEMORANDUM OF MARKS	Rs.10-00
3.	COST OF APPLICATION	Rs.5-00

- Note: (i) Applications are available at the office of the Principal and duly filled-in applications should be handed over in the college office along with necessary fee.
- (ii) Hall tickets will be issued only to the eligible candidates who fulfill the academic requirements of the University.
- (iii) Applications for supplementary examinations should be submitted along with attested photocopies of marks sheets pertaining to earlier attempts made by the student.

Sd/-  
DIRECTOR OF EVALUATION i/c

Date: 06-09-2011

- Note: For applications without late fine, the DDs are to be dated 28.09.2011 and filled in applications have to be submitted at the University Examination Branch on 10.10.2011 only.
- For applications with late fine of Rs.100.00, the DDs are to be dated 01.10.2011 and filled in applications have to be submitted at the University Examination Branch on 10.10.2011 only.
- For late fee of Rs. 1000.00, the DDs are to be dated 03.10.2011 and filled in applications have to be submitted at the University Examination Branch on 10.10.2011 only.



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR  
ANANTAPUR-515002 (A.P)**

**Dr. E.KESHAVA REDDY**  
**Controller of Examinations i/c**

Lr. No. CE / JNTUA/ ANANTAPUR/ M.Tech I & II Semester Regular & Supplementary Examinations  
October/November 2011 dated 06.09.2011

To  
The Principals  
All affiliated Colleges offering M.Tech Program

Sir/Madam,

- Sub:** 1. JNTUA, Anantapur - M.Tech I & II Semester Regular & Supplementary Examinations -  
October/November 2011 - Dispatch of Internal Marks and Award Lists of Laboratory  
Examinations - Due dates - Reg.  
2. Finalization of attendance - Reg.

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1. You are requested to note the following schedule for dispatch of internal marks and/or award lists

Name of the Examination	Data to be sent	Last date for receiving by the Examination Branch
M.Tech II Semester Regular October Examinations	Hard Copy of Internal Marks	19-10-2011
	Soft Copy (CD) of Internal Marks	
	Award Lists of Laboratory End Examinations	
M.Tech II Semester Supplementary October/November 2011 Examinations	Award Lists of Laboratory End Examinations	
M.Tech I Semester Supplementary October/November 2011 Examinations		

2. To finalize the attendance particulars of the students for regular examinations by taking presumptive attendance for the last week of the academic calendar for the semester.

You are hereby informed, by direction that in case the internal marks statements and award lists of the practical examinations are not received by the dates specified above, suitable action will be initiated.

Yours faithfully,

Sd/-  
**Controller of Examinations i/c**



**Jawaharlal Nehru Technological University Anantapur**  
**Examination Branch**  
**Timetable**

**M.Tech - II Semester Regular & Supplementary Examinations October 2011**  
 (For students admitted in 2009-10 & 2010-11 only)

Time: 10.00 AM to 1.00 PM

Date / Day	Electrical Power Engineering (EPE) & Electrical Power Systems (EPS)	Power Electronics and Electrical Drives (PE&ED) & Power Electronics (PE)	VLSI Systems (VLSIS), VLSI System Design (VLSISD), VLSI & VLSID
17-10-2011 Monday	Flexible AC Transmission Systems 9D49201 (Common to EPE, EPS, PE&ED & PE)		Testing & Testability 9D55201 (Common to VLSIS, VLSISD, VLSI, VLSID & ES)
19-10-2011 Wednesday	HVDC Transmission 9D49202 (Common to EPE, EPS, PE&ED & PE)		Low Power VLSI Design 9D57202
21-10-2011 Friday	Operation & Control of Power System 9D49203	Power Electronic Control of AC Drives 9D54203	Algorithms for VLSI Design Automation 9D57203
24-10-2011 Monday	Advanced Power System Protection 9D49204	Advanced Power Semiconductor Devices & Protection 9D54204	FPGA Architectures & Applications 9D55204
27-10-2011 Thursday	Energy Conversion Systems 9D49205	Modern Power Electronics 9D54205	Scripting Language for VLSI Design Automation 9D57205
29-10-2011 Saturday	Programmable Logic Controllers 9D49206a (Common to EPE, EPS, PE&ED & PE)		Nano Electronics 9D57206a
	Energy Auditing, Conservation & Management 9D49206b (Common to EPE, EPS, PE&ED & PE)		Cryptography & Network Security 9D57206b
			Real Time Operating Systems 9D57206c

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**Jawaharlal Nehru Technological University Anantapur**  
**Examination Branch**  
**Timetable**

**M.Tech - II Semester Regular & Supplementary Examinations October 2011**  
 (For students admitted in 2009-10 & 2010-11 only)

Time: 10.00 AM to 1.00 PM

Date / Day	Digital Systems and Computer Electronics (DSCE)	Digital Electronics & Communications Systems (DECS)	Embedded Systems (ES)
17-10-2011 Monday	Micro Computer System Design 9D06201 (Common to DSCE & DECS)		Testing & Testability 9D55201 (Common to VLSIS, VLSISD, VLSI, VLSID & ES)
19-10-2011 Wednesday	Hi-speed Networks 9D06202 (Common to DSCE & DECS)		Real Time Operating Systems 9D55202
21-10-2011 Friday	Design of Fault Tolerant Systems 9D06203	Wireless Communications 9D38203	Hardware Software Co-design 9D55203
24-10-2011 Monday	Systems Programming 9D06204	Coding Theory & Techniques 9D38204	FPGA Architecture & Applications 9D55204
27-10-2011 Thursday	Image & Video Processing 9D06205/9D06205a (Common to DSCE & DECS)		Cryptography & Network Security 9D55205
	-	Optical Communications 9D38206b	
	-	Compression Techniques 9D38206c	
29-10-2011 Saturday	System Modeling & Simulation 9D06206a	Detection & Estimation of Signals 9D38205	Radio Frequency Identification 9D55206a
	Algorithms for VLSI Design Automation 9D06206b		Micro Electromechanical Systems 9D55206b
	FPGA Architecture & Applications 9D06206c		Expert Systems 9D55206c

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**Jawaharlal Nehru Technological University Anantapur**  
**Examination Branch**  
**Timetable**

**M.Tech - II Semester Regular & Supplementary Examinations October 2011**  
 (for students admitted in 2009-10 & 2010-11 only)

Time: 10.00 AM to 1.00 PM

Date / Day	Computer Science and Engineering (CSE) & Computer Science (CS)	Software Engineering (SE)	Machine Design (MD)	CAD/CAM
17-10-2011 Monday	Software Quality Assurance & Testing 9D58201 (Common to CSE, CS & SE)		Advanced Optimization Techniques 9D04201 (Common to MD and CAD/CAM)	
19-10-2011 Wednesday	Object Oriented Analysis & Design 9D58202	Software Architecture & Design Patterns 9D25202	Robotics 9D04202 (Common to MD and CAD/CAM)	
21-10-2011 Friday	Advanced Computer Networks 9D58203	Service Oriented Architecture 9D25203	Mechanical Vibrations 9D15203	Computer Graphics 9D04203
24-10-2011 Monday	Distributed Systems 9D58204	Software Reliability 9D25204	Experimental Stress Analysis 9D15204	CNC Technology & Programming 9D04204
27-10-2011 Thursday	Data Warehousing & Mining 9D58205	Software Reengineering 9D25205	Theory of Plasticity 9D15205	Mechatronics 9D04205
29-10-2011 Saturday	Software Architecture 9D58206a	Secure Software Engineering 9D25206a	Design for Manufacturing 9D15206a	Rapid Prototyping 9D04206a
	Software Design 9D58206b	Soft Computing 9D25206b	Pressure Vessel Design 9D15206b	Artificial Intelligence & Expert Systems 9D04206b
	Design Patterns 9D58206c	Model Driven Software Development 9D25206c	Mechanics of Composite Materials 9D15206c	Mechanics & Manufacturing methods of Composites 9D04206c

NOTE: 1. Any clashes / omissions in this time-table may be brought to the notice of the under signed immediately.  
 2. If the Government declares holiday on any of the above dates, the examinations will be conducted as usual.

Date: 06-09-2011

Sd/-  
Controller of Examinations i/c



**Jawaharlal Nehru Technological University Anantapur**  
**Examination Branch**  
**Timetable**

**M.Tech - II Semester Regular Examinations October 2011**  
**(For students admitted in 2010-11 only)**

**Time: 10.00 AM to 1.00 PM**

<b>Date / Day</b>	<b>Computer Networks (CN)</b>	<b>Communication Systems (CS)</b>	<b>Geotechnical Engineering (GE)</b>
17-10-2011 Monday	Wireless Networks 9D08201	Wireless Communications and Fading 9D61201	Deep Foundations and Earth Retaining Structures 9D12201
19-10-2011 Wednesday	Mobile Adhoc Networks 9D08202	Digital Satellite Communications 9D61202	Ground Improvement Techniques 9D12202
21-10-2011 Friday	Peer to Peer Networks 9D08203	Adaptive Filter Theory 9D61203	Design with Geo-Synthetics 9D12203
24-10-2011 Monday	Optical Networks 9D08204	Optical Fiber Communications 9D61204	Finite Element Method for Geo-Technical Applications 9D12204
27-10-2011 Thursday	Cryptography and Network Security 9D08205	MIMO Communication Systems 9D61206a	Earth and Rockfill Dams and Slope Stability 9D12205a
		Secure Communication 9D61206b	Soil Stabilization and Flexible Pavement Design 9D12205b
		Image and Video Processing 9D06205 (Common to DSCE, DECS & CS)	
29-10-2011 Saturday	Wireless Network Security 9D08206a	RF Systems and Circuits 9D61205	Experimental Geo-Mechanics 9D12206a
	Trustworthy Computing 9D08206b		Structural Analysis and Foundation Design 9D12206b
	Software Architecture 9D58206a (Common to CSE, CS & CN)		

NOTE: 1. Any clashes / omissions in this time-table may be brought to the notice of the under signed immediately.  
 2. If the Government declares holiday on any of the above dates, the examinations will be conducted as usual.

Date: 06-09-2011

Sd/-  
 Controller of Examinations i/c

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR**  
ANANTAPUR - 515 002 (A.P.) - INDIA.



**EXAMINATION BRANCH**

**M.Tech. (DT) - II Semester Supplementary Examinations - October/November 2011**  
(for students admitted in 2007-08 & 2008-09 only)

**Timetable**

**Time: 10:00 AM to 01:00 PM**

Date / Day	Electrical Power Engineering	Electrical Power Systems	Power Electronics	Digital Systems and Computer Electronics	Digital Electronics & Communication Systems	Embedded Systems	VLSI Design, VLSI Systems Design	Computer Science & Engineering	Computer Science	Software Engineering	CAD / CAM
17-10-2011 Monday	Neural & Fuzzy Systems B4901 (Common to EPE, EPS, PE)	Neural & Fuzzy Systems B0704 (Common to EPE, EPS, PE)	Neural & Fuzzy Systems B4304 (Common to EPE, EPS, PE)	Algorithms for VLSI Design Automation (Elective-IV) B0609 (Common to VLSID, VLSISD, DSCE)	Coding Theory & Techniques B3803	Real Time Operating Systems for Embedded Systems B5501	Algorithms for VLSI Design Automation B5701 (Common to VLSID, VLSISD, DSCE)	Data Warehousing & Mining B5801 (Common to CSE, CS, SE)	Data Warehousing & Mining B0502 (Common to CSE, CS, SE)	Data Warehousing & Mining B2502 (Common to CSE, CS, SE)	Computer Aided Manufacturing B0401
19-10-2011 Wednesday	Advanced Power System Protection B4902 (Common to EPE, EPS)	Advanced Power System Protection B0702 (Common to EPE, EPS)	Microprocessor & Micro-controllers B4302	Image Processing B0602 (Common to DSCE, DECS)	Image Processing B3801 (Common to DSCE, DECS)	Embedded Software Design B5504	Advanced Computer Architecture (Elective-IV) B5710 (Common to VLSID, VLSISD, CSE)	Advanced Computer Architecture (Elective-I) B5807 (Common to VLSID, VLSISD, CSE)	Compiler Design B0501	Information Security B2501	Flexible Manufacturing Systems B0402
21-10-2011 Friday	Flexible AC Transmission Systems B4904 (Common to EPE, PE)	High Voltage DC Transmission B0703	Flexible AC Transmission Systems B4303 (Common to EPE, PE)	Speech Processing B0603 (Common to DSCE, DECS)	Speech Processing B3802 (Common to DSCE, DECS)	CPLD and FPGA Architecture & Applications B5503	-	Mobile Computing (Elective-II) B5809 Software Project Management (Elective-II) B5810	Software Engineering B0503	Software Quality Assurance & Testing B2503	Industrial Robotics B0407
24-10-2011 Monday	-	Power System Control & Stability B0701	Power Electronics Control of AC Drives B4301	Embedded and Real Time Systems B0604	Micro Computer Systems Design B3804	Scripting Language for VLSI Design Automation (Elective-III) B5505 (Common to ES, VLSID, VLSISD)	Scripting Language for VLSI Design Automation B5704 (Common to ES, VLSID, VLSISD)	Object Oriented Analysis & Design B5804 (Common to CSE, CS)	Object Oriented Analysis & Design B0504 (Common to CSE, CS)	Software Architecture & Design Pattern B2504	Design of Hydraulics & Pneumatic Systems B0405

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**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR**  
ANANTAPUR - 515 002 (A.P.) - INDIA.



**EXAMINATION BRANCH**

**M.Tech. (DT) - II Semester Supplementary Examinations - October/November 2011**  
(for students admitted in 2007-08 & 2008-09 only)

**Timetable**

**TIME: 10:00 AM To 01:00 PM**

Date / Day	Electrical Power Engineering	Electrical Power Systems	Power Electronics	Digital Systems and Computer Electronics	Digital Electronics & Communication Systems	Embedded Systems	VLSI Design, VLSI Systems Design	Computer Science & Engineering	Computer Science	Software Engineering	CAD / CAM		
27-10-2011 Thursday	Advanced Digital Signal Processing (Elective-III) B4905 (Common to EPE, EPS, PE)	Advanced Digital Signal Processing (Elective-II) B0707 (Common to EPE, EPS, PE)	Advanced Digital Signal Processing (Elective-III) B4307 (Common to EPE, EPS, PE)	CMOS Analog & Mixed Signal Design (Elective-III) B0605	High Speed Networks (Elective-III) B3807 (Common to DSCE, DECS)	Advanced Digital Filtering (Elective-III) B5507	Hardware Software Co-Design (Elective-III) B5705	Network Security & Cryptography B5803 (Common to VLSID, VLSISD, CSE, CS)	Web Technologies (Elective-I) B0506	Human Computer Interaction (Elective-I) B2505	Automation in Manufacturing B0406		
	Distribution Automation (Elective-III) B4906	Extra High Voltage Transmission (Elective-II) B0706	Digital Control Systems (Elective-III) B4305 (Common to EPE, PE)	High Speed Networks (Elective-III) B0606 (Common to DSCE, DECS)			Network Security & Cryptography (Elective-III) B5707 (Common to VLSID, VLSISD, CSE, CS)		Network Security & Cryptography (Elective-I) B0507 (Common to VLSID, VLSISD, CSE, CS)			Image Processing and Pattern Recognition (Elective-I) B2507	
	Digital Control Systems (Elective-III) B4907 (Common to EPE, PE)						Advanced Operating Systems (Elective-IV) B5709 (Common to VLSID, VLSISD)		Neural Networks (Elective-I) B5806 (Common to CSE, SE, CS)			Neural Networks (Elective-II) B0510 (Common to CSE, SE, CS)	Multimedia & Application Development (Elective-II) B2509
29-10-2011 Saturday	Enterprise Resource Planning (Elective-IV) B4908 (Common to EPE, PE)	Power System Reliability B0705	Enterprise Resource Planning (Elective-IV) B4309 (Common to EPE, PE)	Wireless Communications & Networks (Elective-IV) B0608 (Common to DSCE, DECS, ES)	Wireless Communications & Networks (Elective-IV) B3809 (Common to DSCE, DECS, ES)	Wireless Communications & Networks (Elective-IV) B5510 (Common to DSCE, DECS, ES)	Advanced Operating Systems (Elective-IV) B5709 (Common to VLSID, VLSISD)	Neural Networks (Elective-I) B5806 (Common to CSE, SE, CS)	Neural Networks (Elective-II) B0510 (Common to CSE, SE, CS)	Neural Networks (Elective-II) B2508 (Common to CSE, SE, CS)	Intelligent Manufacturing Systems (Elective-II) B0409		
	Power System Operation & Control (Elective-IV) B4909		Special Machines Control (Elective-IV) B4310							Radar Signal Processing (Elective-IV) B3810	Micro Electromechanical Systems (Elective-IV) B5509	Information Retrieval Systems (Elective-II) B2510	Computational Fluid Dynamics (Elective-II) B0404
	Reliability Engineering (Elective-IV) B4910 (Common to EPE, PE)		Reliability Engineering (Elective-IV) B4308 (Common to EPE, PE)										

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**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR  
ANANTAPUR - 515 002 (A.P.) - INDIA.**



**EXAMINATION BRANCH**

**M.Tech. (DT) - II Semester Supplementary Examinations - October/November 2011  
(for students admitted in 2007-08 & 2008-09 only)**

**Timetable**

**TIME: 10:00 AM To 01:00 PM**

Date / Day	Electrical Power Engineering	Electrical Power Systems	Power Electronics	Digital Systems and Computer Electronics	Digital Electronics & Communication Systems	Embedded Systems	VLSI Design, VLSI Systems Design	Computer Science & Engineering	Computer Science	Software Engineering	CAD / CAM
01-11-2011 Tuesday	Power Quality B4903 (Common to EPE,PE)	-	Power Quality (Elective-III) B4306 (Common to EPE,PE)	Design of Fault Tolerant Systems B0601 (Common to DSCE,DECS)	Design of Fault Tolerant Systems (Elective-III) B3805 (Common to DSCE,DECS)	Design for Testability (Elective-IV) B5508 (Common to ES, VLSID,VLSISD)	Design for Testability B5702 (Common to ES, VLSID,VLSISD)	Embedded Systems B5802 (Common to CSE, CS)	Embedded Systems (Elective-II) B0509 (Common to CSE, CS)	-	-
03-11-2011 Thursday	-	-	-	-	Low Power VLSI Design (Elective-III) B3806 (Common to DECS,ES,VLSID, VLSISD)	Low Power VLSI Design (Elective-III) B5506 (Common to DECS,ES,VLSID,VLSISD)	Low Power VLSI Design B5703 (Common to DECS,ES,VLSID,VLSISD)	Middleware Technologies (Elective-II) B5808 (Common to CSE,CS,SE)	Middleware Technologies (Elective-II) B0508 (Common to CSE,CS,SE)	Middle Ware Technologies (Elective-I) B2506 (Common to CSE,CS,SE)	-
05-11-2011 Saturday	-	-	-	DSP Processors & Architectures (Elective-III) B0607 (Common to DSCE,DECS,ES, VLSID,VLSISD)	DSP Processors & Architectures (Elective-IV) B3808 (Common to DSCE,DECS,ES, VLSID,VLSISD)	DSP Processors & Architectures B5502 (Common to DSCE,DECS,ES, VLSID,VLSISD)	DSP Processors & Architectures (Elective-IV) B5708 (Common to DSCE,DECS,ES, VLSID,VLSISD)	Pattern Recognition & Image Processing (Elective-I) B5805 (Common to CSE,CS)	Pattern Recognition & Image Processing (Elective-I) B0505 (Common to CSE,CS)	-	-
08-11-2011 Tuesday	-	-	-	System Modeling & Simulation (Elective-IV) B0610 (Common to DSCE,VLSID, VLSISD)	-	-	System Modeling & Simulation (Elective-III) B5706 (Common to DSCE,VLSID,VLSISD)	-	-	-	-

Note: (i) Any omissions or clashes in this time table may please be informed to the Controller of Examinations immediately.

(ii) Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual.

Date: 06-09-2011

Sd/-  
Controller of Examinations i/c



**Jawaharlal Nehru Technological University Anantapur**  
**Examination Branch**  
**Timetable**

**M.Tech - I Semester Supplementary Examinations October/November 2011**  
 (For students admitted in 2009-10 & 2010-11 only)

**Time: 10.00 AM to 01.00 PM**

Date / Day	Supplementary		
	Electrical Power Engineering (EPE) & Electrical Power Systems (EPS)	Power Electronics and Electrical Drives (PE&ED) & Power Electronics (PE)	VLSI Systems (VLSIS), VLSI System Design (VLSISD), VLSI & VLSID
18-10-2011 Tuesday	Modern Control Theory 9D49101 (Common to EPE, EPS, PE&ED & PE)		VLSI Technology 9D57101 (Common to VLSIS, VLSISD, VLSI, VLSID & ES,)
20-10-2011 Thursday	Microprocessors & Microcontrollers 9D49102 (Common to EPE, EPS, PE&ED & PE)		Analog IC Design 9D57102
22-10-2011 Saturday	Power System Control & Stability 9D49103	Principles of Machine Modeling Analysis 9D54103	Embedded Systems Concepts 9D57106a
25-10-2011 Tuesday	EHVAC Transmission 9D49104	Analysis of Power Electronic Converters 9D54104	Hardware Description Languages 9D57104
28-10-2011 Friday	Reactive Power Compensation & Management 9D49105	Power Electronic Control of DC Drives 09D54105	Hardware Software Co-design 9D57105
31-10-2011 Monday	Advanced Digital Signal Processing 9D49106a (Common to EPE, EPS, PE&ED & PE)		System Modeling & Simulation 9D57106b
			ASIC Design 9D57106c
02-11-2011 Wednesday	Neural Networks and Fuzzy Systems 9D49106b (Common to EPE, EPS, PE& ED & PE)		Digital IC Design 9D57103 (Common to VLSIS, VLSISD, VLSI & ES)



**Jawaharlal Nehru Technological University Anantapur**  
**Examination Branch**  
**Timetable**

**M.Tech - I Semester Supplementary Examinations October/November 2011**  
**(For students admitted in 2009-10 & 2010-11 only)**

**Time: 10.00 AM to 01.00 PM**

Date / Day	Supplementary		
	Digital Systems and Computer Electronics (DSCE)	Digital Electronics & Communications Systems (DECS)	Embedded Systems
18-10-2011 Tuesday	Digital System Design 9D06101 (Common to DSCE & DECS)		VLSI Technology 9D57101 (Common to VLSIS, VLSISD, VLSI, VLSID & ES)
20-10-2011 Thursday	Embedded System Concepts 9D06102 (Common to DSCE, DECS, & ES)		
22-10-2011 Saturday	Advanced Computer Architecture 9D06103 (Common to DSCE, DECS, & ES)		
25-10-2011 Tuesday	Advanced Data Communications 9D06104	Digital Communication Techniques 9D38104	Micro Controllers & Interfacing 9D55104
28-10-2011 Friday	Neural Networks & Applications 9D06105	Adaptive Signal Processing 9D38105	Operating Systems 9D55105
31-10-2011 Monday	DSP Processors & Architectures 9D06106b (Common to DSCE & DECS)		Advanced DSP Applications 9D55101
	Lower Power VLSI Design 9D06106c (Common to DSCE & DECS)		
02-11-2011 Wednesday	Network Security & Cryptography 9D06106a	Advanced Digital Signal Processing 9D38103	Digital IC Design 9D57103 (Common to VLSIS, VLSISD, VLSI & ES)
			Algorithms for VLSI Design Automation 9D55106c

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**Jawaharlal Nehru Technological University Anantapur**  
**Examination Branch**  
**Timetable**

**M.Tech - I Semester Supplementary Examinations October/November 2011**  
 (For students admitted in 2009-10 & 2010-11only)

Time: 10.00 AM to 01.00 PM

Date / Day	Computer Science and Engineering (CSE) & Computer Science (CS)	Software Engineering (SE)	Machine Design (MD)	CAD/CAM
18-10-2011 Tuesday	Advanced Data Structures & Algorithms 9D58101 (Common to CSE, CS, SE & CN)		Computational Methods 9DBS101 (Common to MD and CAD/CAM)	
20-10-2011 Thursday	Discrete Structures 9D58102	Advanced Software Engineering 9D25102	Advanced Mechanisms 9D15102	Finite Element Methods 9D04102
22-10-2011 Saturday	Computer System Design 9D58103	Software Requirements & Estimation 9D25103	Advanced Mechanics of Solids 9D15103	Computer Integrated Manufacturing 9D04103
25-10-2011 Tuesday	Java & Web Technologies 9D58104	Software Metrics 9D25104	Fracture, Fatigue & Creep Deformation 9D15104	Geometric Modeling 9D04104
28-10-2011 Friday	Software Engineering 9D58105	Middleware Technologies 9D25105	Materials Technology 9D15105	Advances in Manufacturing Technology 9D04105
31-10-2011 Monday	Advances in Databases 9D58106a	Object Oriented Modeling 9D25106a	Tribology 9D15106a	Quality Engineering & Manufacturing 9D04106a
	Distributed Databases 9D58106b	Software Project Management 9D25106b	Gear Engineering 9D15106b	Computer Aided Process Planning 9D04106b
	Computer Vision 9D58106c	Software Process Management 9D25106c	Non Destructive Evaluation 9D15106c	Design for Manufacturing 9D04106c

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**Jawaharlal Nehru Technological University Anantapur**  
**Examination Branch**  
**Timetable**

**M.Tech - I Semester Supplementary Examinations October/November 2011**  
 (For students admitted in 2009-10 & 2010-11 only)

**Time: 10.00 AM to 01.00 PM**

Date / Day	Supplementary		
	Computer Networks (CN)	Communication Systems (CS)	Geotechnical Engineering (GE)
18-10-2011 Tuesday	Advanced Data Structures & Algorithms 9D58101 (Common to CSE, CS, SE & CN)	Advanced Mathematics for Communication Systems 9D61101	Advanced Soil Mechanics 9D12101
20-10-2011 Thursday	Advanced Computer Networks 9D58203	Modern Digital Communication Techniques 9D61102	Theoretical Soil Mechanics 9D12102
22-10-2011 Saturday	Distributed Systems 9D58204	Detection & Estimation of Signals 9D38205	Numerical Methods 9D12103
25-10-2011 Tuesday	Network Management Systems 9D08103	Computer Communication Networks 9D61103	Shallow Foundations 9D12104
28-10-2011 Friday	Protocol Software Engineering 9D08104	Information & Coding Techniques 9D61104	Geo-Technical Earth Quake Engineering 9D12105a
			Geographical Information systems 9D12105b
31-10-2011 Monday	Performance Evaluation of Computer Networks 9D08105a	Digital Voice & Picture Communication 9D61105a	Geo-Environmental Engineering 9D12106a
	Grid Computing 9D08105b	Mobile Ad-hoc Networks 9D08202	Soil Dynamics & Machine Foundations 9D12106b
	Secure Software Engineering 9D25206a	Radar Signal Processing 9D61105b	

- NOTE: 1. Any clashes / omissions in this time-table may be brought to the notice of the under signed immediately.  
 2. If the Government declares holiday on any of the above dates, the examinations will be conducted as usual.

Date: 06-09-2011

Sd/-  
 Controller of Examinations i/c



**Jawaharlal Nehru Technological University Anantapur**  
**Examination Branch**  
**Timetable**

**M.Tech - I Semester Supplementary Examinations October 2011**  
 (for students admitted in 2007-08 & 2008-09 only)

**Time: 10.00 AM to 01.00 PM**

Date / Day	Electrical Power Engineering	Electrical Power Systems	Power Electronics
18-10-2011 Tuesday	Power System Dynamics A4901	Distribution Automation A0701	Machine Modeling & Analysis A4301
20-10-2011 Thursday	High Voltage DC Transmission A4902	Reliability Engineering A0702	Analysis of Power Electronic Converters A4302
22-10-2011 Saturday	Modern Control Theory A4903	Flexible AC Transmission Systems A0704	Modern Control Theory A4303
25-10-2011 Tuesday	Microprocessors & Microcontrollers A4904	Microprocessors & Microcontrollers A0703	Power Electronic Control of DC Drives A4304
28-10-2011 Friday	High Voltage Engineering & Insulation Coordination A4905	Advanced Control Systems A0705	HVDC Transmission A4305
	Voltage Stability A4906		Modern Power Electronics A4306
	Operation Research A4907		Operation Research A4307
31-10-2011 Monday	Analysis of Power Electronic Converters A4908	Reactive Power Compensation & Management A0706	Programmable Logic Controllers and it's applications A4308
	Energy Conservation Systems A4909	Energy Auditing , Conservation & Management A0707	Energy Conversion Systems A4309
	Extra High Voltage Transmission A4910		Dynamics of Electrical Machine A4310

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**Jawaharlal Nehru Technological University Anantapur**  
**Examination Branch**  
**Timetable**  
**M.Tech - I Semester Supplementary Examinations October 2011**  
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**Time: 10.00 AM to 01.00 PM**

Date / Day	Digital Systems and Computer Electronics (DSCE)	Digital Electronics & Communications Systems (DECS)	Embedded Systems	VLSI Design & VLSI System Design
18-10-2011 Tuesday	Digital System Design A0601	Digital System Design A3801	Micro Controllers for Embedded System Design A5501	Digital System Design A5702
20-10-2011 Thursday	VLSI Technology & Design A0602	Advanced Digital Signal Processing A3802	ELECTIVE VLSI Technology & Design A5505 Advanced Computer Architecture A5506 RF and Microwave Integrated Circuits A5507	VLSI Technology & Design A5701
22-10-2011 Saturday	ELECTIVE High Performance TCP/IP Networks A0609 Advanced Computer Architecture A0608 Low Power VLSI Design A0610	Detection & Estimation of Signals A3803	Embedded Systems Concepts A5503	ELECTIVE VHDL Modeling of Digital Systems A5708 Modeling and Synthesis with Verilog HDL A5709 Embedded Systems Concepts A5710
25-10-2011 Tuesday	Digital Data Communications A0603	Digital Data Communications A3804	ELECTIVE Digital Data Communications A5508 Algorithm Analysis & Design A5509 Image and Video Processing A5510	ELECTIVE Digital Data Communications A5706 CPLD and FPGA Architecture & Applications A5707 Computational Techniques in Micro Electronics A5705
28-10-2011 Friday	Neural Networks & Applications A0604	ELECTIVE Embedded & Real Time Systems A3805 Network Security & Cryptography A3806 Neural Networks & Applications A3807	System Modeling & Simulation A5504	Electronic Design Automation Tools A5704
31-10-2011 Monday	ELECTIVE Advanced Operating Systems A0605 Network Security & Cryptography A0607 Artificial Intelligence A0606	ELECTIVE Advanced Operating Systems A3808 Advanced Computer Architecture A3809 Artificial Intelligence A3810	Analog & Digital IC Design A5502	Analog IC Design A5703

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**Jawaharlal Nehru Technological University Anantapur**  
**Examination Branch**  
**Timetable**

**M.Tech - I Semester Supplementary Examinations October 2011**  
 (for students admitted in 2007-08 & 2008-09 only)

Time: 10.00 AM to 01.00 PM

Date / Day	Computer Science and Engineering	Computer Science	Software Engineering	CAD/CAM
18-10-2011 Tuesday	Design & Analysis of Algorithms A5801	Design & Analysis of Algorithms A0501	Software Requirements & Estimation A2501	Numerical Methods for Partial Differential Equations A0401
20-10-2011 Thursday	Computer Organization A5802	Computer Organization A0502	Unix Network Programming A2502	Computer Aided Design A0402
22-10-2011 Saturday	Computer Communications A5803	Computer Communications A0503	Software Process and Project Management A2503	Stress Analysis & Vibration A0403
25-10-2011 Tuesday	Operating Systems A5804	Operating Systems A0504	Web Technologies A2504	Finite Element Analysis A0404
28-10-2011 Friday	Software Engineering A5805	Mathematical Foundations for Computer Science A0505	Wireless Networks & Mobile Computing A2505	Design Optimization A0405
31-10-2011 Monday	Database Management Systems A5806	Database Management Systems A0506	Object Oriented Modeling A2506	Mechatronics A0406
				Design for Manufacturing A0407
				Manufacturing Methods & Mechanics of Composites A0408

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