



TIME: 10:00AM TO 01:00 PM

TIME-TABLE

Date / Day	Civil Engineering (CE)	Electrical & Electronics Engineering (EEE)	Mechanical Engineering (ME)	Electronics & Communication Engineering (ECE)	Electronics & Instrumentation Engineering (EIE)	Electronics & Control Engineering (E.Con.E)	Electronics & Computer Engineering (ECC)	Computer Science & Engineering (CSE)	Information Technology (IT)	Computer Science & Systems Engineering (CSS)	Bio-Medical Engineering (BME)	Bio-Technology (BT)	Instrumentation & Control Engineering (ICE)
17-07-09 Friday	Advanced Communication Skills (R5420107)	Digital Control Systems (R5420202)	Total Quality Management (R5420305)	---	Management Information Systems (R5421004) (EIE,E.Con.E)	---	Client Server Computing (R5421902)	Virtual Reality (R5420505)	Bio-Informatics (R5421205)	Design Patterns (R5421206) (IT,CSS)	Robotics & Automation (R5421104)	Biopharmaceutical Technology (R5422305)	Opto-Electronic & Laser Instrumentation (R5422206)
	Advanced Structural Analysis (R5420108)	H.V.D.C Transmission (R5420203)	Robotics (R5420306)	DSP Processors & Architectures (R5420406) (ECE,EIE, & BME)	Telemetry & Telecontrol (R5421005) (EIE,E.Con.E)	Telemetry & Telecontrol (R5421005) (EIE, E.Con.E)	Information Security (R5421903)	Human Computer Interface (R5420506)	Design Patterns (R5421206) (IT,CSS)	Software Testing Methodologies (R5421506)	DSP Processors & Architectures (R5420406) (ECE,EIE, & BME)	Photochemical & Herbal Medicine (R5422305)	
	Design & Drawing of Hydraulic Structures (R5420109)	Embedded Systems (R5420204)	Computer Graphics (R5420307)	Artificial Neural Networks (R5420407)	DSP Processors & Architectures (R5420406) (ECE,EIE & BME)	Computer Aided Design Of Control Systems (R5421306)	---	Advanced Computing Concepts (R5420507)	Pattern Recognition (R5421207) (IT,CSS)	Pattern Recognition (R5421207) (IT,CSS)	Quantitative Engineering Physiology (R5421107)		



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY ANANTAPUR
ANANTAPUR – 515 002 (A.P.) – INDIA.

EXAMINATION BRANCH
IV B.Tech. – II Semester (R05) – Advanced Supplementary Examinations – July 2009

TIME: 10:00AM TO 01:00 PM

TIME-TABLE

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20-07-09 Monday	---	---	---	Digital Design Through Verilog (R5420405) (ECE,ECC, BME)	Industrial Electronics (R54210003) (EIE, E.Con.E)	Industrial Electronics (R54210003) (EIE, E.Con.E)	Digital Design Through Verilog (R5420405) (ECE,ECC, BME)	---	---	---	Digital Design Through Verilog (R5420405) (ECE,ECC, BME)	---	---
22-07-2009 Wednesday	---	---	---	Wireless Communications & Networks (R5420404) (ECE,EIE, ECC)	Wireless Communications & Networks (R5420404) (ECE,EIE,ECC)	Wireless Communications & Networks (R5420404) (ECE,EIE,ECC)	Wireless Communications & Networks (R5420404) (ECE,EIE,ECC)	---	---	---	---	---	---

Note: (i) Any omissions or clashes in this time table may please be informed to the undersigned immediately.
(ii) Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual.


CONTROLLER OF EXAMINATIONS i/c

Date: 29.06.2009